

High-Efficiency, 40V White LED Driver with Dimming Control

Description

The FP6745 is a step-up DC/DC converter specifically designed for driving WLEDs with a constant current. The FP6745 can drive up to 10 white LEDs in series from a Lithium Ion battery. Series connection of LEDs provides identical LED current for uniform brightness and minimizes the number of traces to the LEDs. The FP6745 uses current mode, fixed frequency of approximately 1.3MHz architecture to regulate the LED current through an external current sense resistor. The low feedback voltage of 195mV can minimize power dissipation.

Other features include current limit protection, thermal shutdown protection, under-voltage lockout (UVLO), and over-voltage function, which can shut off the device if output voltage reaches above 41V. The FP6745 is available in space saving TSOT-23-5, TSOT-23-6 and SOT-23-6 packages.

Features

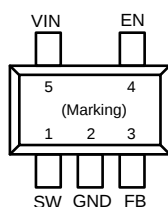
- Wide Range for PWM Dimming, Ranging from 100Hz to 50kHz
- High Efficiency: 87%
- Drives up to 10 WLEDs
- Fast 1.3MHz Switching Frequency
- Low 195mV Feedback Voltage
- Over Voltage Protection
- Low Profile TSOT-23-5, TSOT-23-6 and SOT-23-6 Packages
- RoHS Compliant

Applications

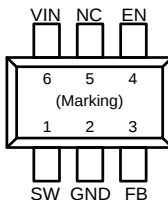
- Cellular Phone
- Digital Camera
- LCD Panel Backlight
- GPS Receiver
- PDA, Handheld Computer

Pin Assignments

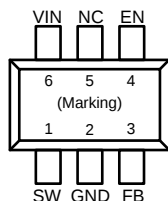
S8 Package (TSOT-23-5)



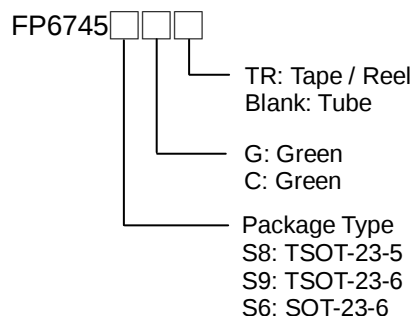
S9 Package (TSOT-23-6)



S6 Package (SOT-23-6)



Ordering Information



TSOT-23-5 Marking

Part Number	Product Code
FP6745S8G	P4=

TSOT-23-6 Marking

Part Number	Product Code
FP6745S9G	P5=

SOT-23-6 Marking

Part Number	Product Code
FP6745S6C	A8G

Figure 1. Pin Assignment of FP6745

Typical Application Circuit

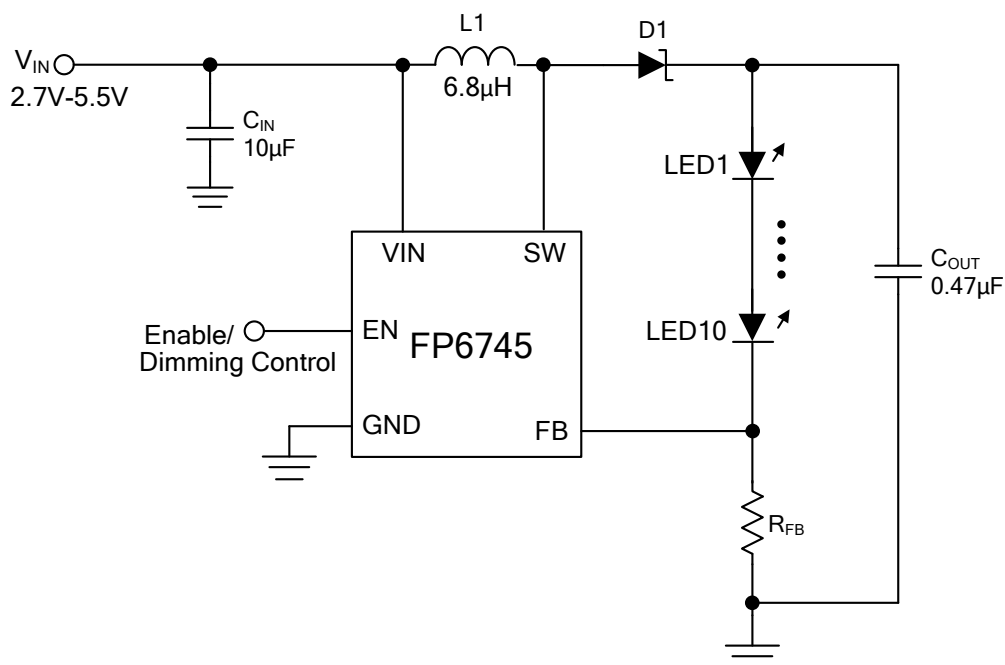


Figure 2. Typical Application Circuit of FP6745

Functional Pin Description

Pin Name	Pin No. (TSOT-23-6)	Pin No. (TSOT-23-5)	Pin Function
SW	1	1	Switch Pin. Connect inductor/diode here. Minimize trace area at this node to reduce EMI.
GND	2	2	Ground Pin. Connect directly to local ground plane.
FB	3	3	Feedback Pin. Reference voltage is 195mV. Connect cathode of the lowest LED and resistor here. Calculate resistor value according to the formula: $R_{FB} = V_{FB} / I_{LED}$
EN	4	4	Enable and dimming control. 1. Enable: Logic high enables the device; logic low forces the device into shutdown mode. 2. Analog dimming control: apply 0.7V to 1.4V DC voltage signal. 3. Digital dimming control: apply external 100Hz to 50kHz PWM pulse signal with amplitude greater than 1.5V.
NC	5	-	No connection.
VIN	6	5	Input Supply Pin. Must be locally bypassed.

Block Diagram

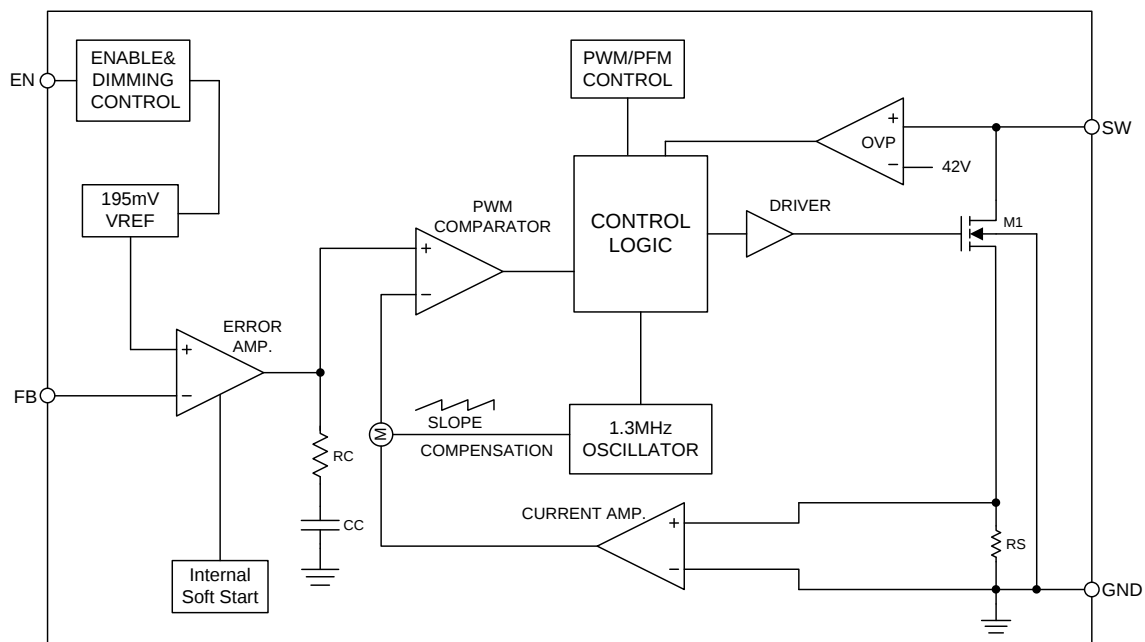


Figure 3. Block Diagram of FP6745

Absolute Maximum Ratings ^(Note 1)

- VIN ----- -0.3V to +6V
- SW Voltage ----- -0.3V to +43V
- Dynamic SW Voltage in 15ns Duration ----- -5V to 45V
- FB Voltage ----- -0.3V to +6V
- EN Voltage ----- -0.3V to +6V
- Maximum Junction Temperature (T_J) ----- +150°C
- Power Dissipation @ $T_A=25^{\circ}\text{C}$, (P_D)
 - TSOT-23-5/TSOT-23-6/SOT-23-6 ----- 0.5W
- Package Thermal Resistance, (θ_{JA}) ^(Note 2)
 - TSOT-23-5/TSOT-23-6/SOT-23-6 ----- 250°C/W
- Package Thermal Resistance, (θ_{JC}) ^(Note 2)
 - TSOT-23-5 ----- 130°C/W
 - TSOT-23-6 ----- 110°C/W
 - SOT-23-6 ----- 110°C/W
- Storage Temperature Range (T_S) ----- -65°C to +150°C
- Lead Temperature (Soldering, 10 sec.) (T_{LEAD}) ----- +260°C

Note 1: Stresses beyond this listed under "Absolute Maximum Ratings" may cause permanent damage to the device.

Note 2: θ_{JA} is measured at 25°C ambient with the component mounted on a high effective thermal conductivity 4-layer board of JEDEC-51-7. θ_{JC} is measured at the exposed pad. The thermal resistance greatly varies with layout, copper thickness, number of layers and PCB size.

Recommended Operating Conditions

- Input Voltage (V_{IN}) ----- +2.5 to +5.5V
- Operating Temperature Range ----- -40°C to +85°C

Electrical Characteristics

($V_{IN}=V_{EN}=5V$, $T_A=+25^{\circ}C$, unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Operating Input Voltage	V_{IN}		2.5		5.5	V
Operation Current	I_{SD}	$V_{EN}=0V$		4	8	μA
	I_Q	$V_{FB}=0.2V$		430	550	μA
Switching Frequency	f_{SW}		1.0	1.3	1.5	MHz
Maximum Duty Cycle	Duty	$V_{FB}=0V$	92			%
Under Voltage Lockout						
VIN Under Voltage Lockout	UVLO	V_{IN} Rising		2.25	2.45	V
Under Voltage Lockout Hysteresis				92		mV
Open Lamp Shutdown Threshold	V_{OV}	V_{SW} Rising	41	42		V
Enable						
EN OFF Threshold		V_{EN} Falling	0.4			V
EN ON Threshold		V_{EN} Rising			0.6	V
Minimum EN Dimming Threshold		$V_{FB}=0V$		0.7		V
Maximum EN Dimming Threshold		$V_{FB}=195mV$		1.4		V
Dimming On Time	T_{ON}	$V_{IN}=5V$	1.5			μs
Feedback						
FB Voltage	V_{FB}	$V_{EN}=1.5V$	185	195	205	mV
FB Input Bias Current	I_{FB}	$V_{FB}=0.1V$		-300		nA
Output Switch						
SW ON-Resistance ^(Note 3)	R_{ON}			0.5		Ω
SW Current Limit ^(Note 3)	I_{LM}			1.33		A
Thermal Shutdown ^(Note 3)	T_{SD}			150		$^{\circ}C$

Note 3: The specification is guaranteed by design, not production tested.

Typical Performance Curves

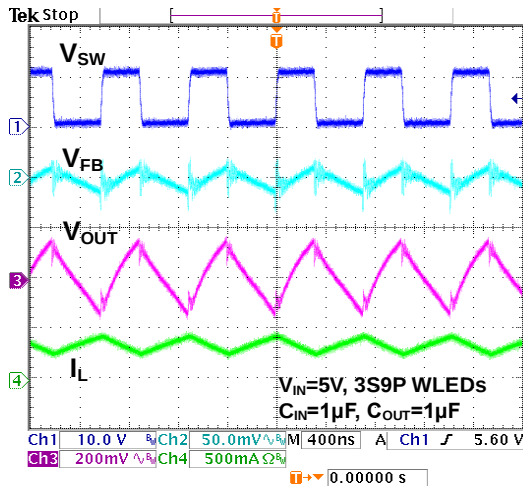


Figure 4. Switch waveforms

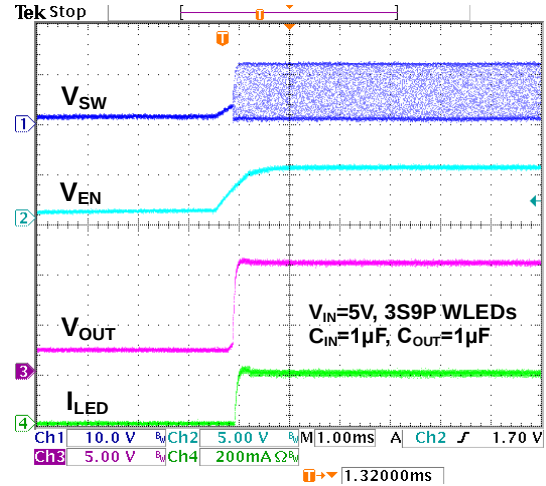


Figure 5. Enable Startup Response Waveforms

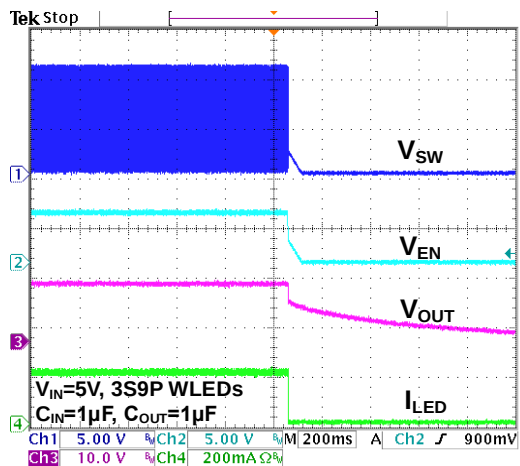


Figure 6. Enable Shutdown Response Waveforms

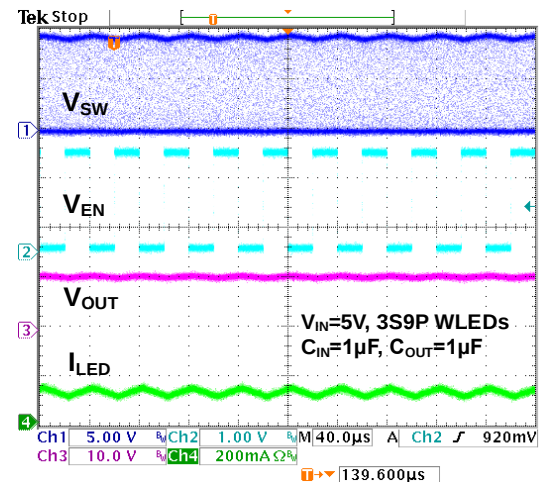


Figure 7. PWM Dimming Response Waveforms at a frequency of 25kHz

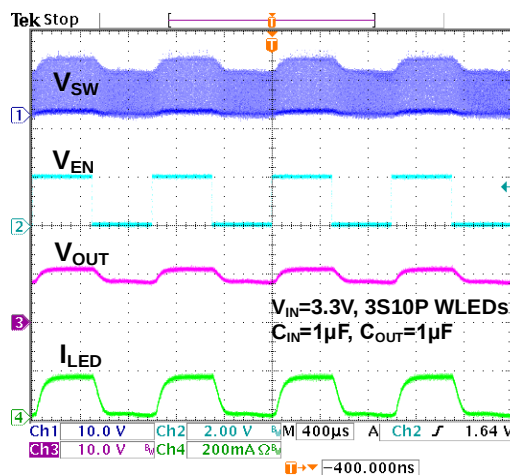


Figure 8. PWM Dimming Response Waveforms at a frequency of 1kHz

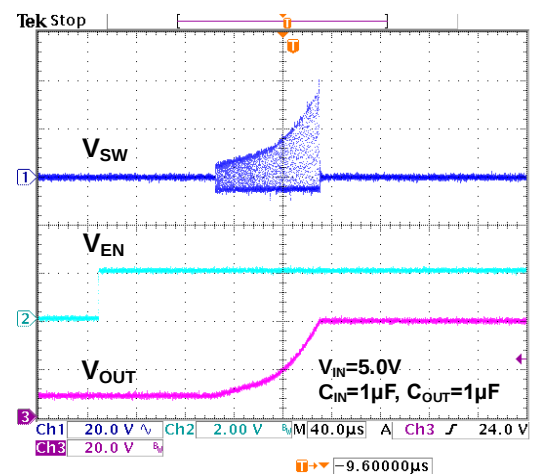


Figure 9. Open load Protection Waveforms

Typical Performance Curves (Continued)

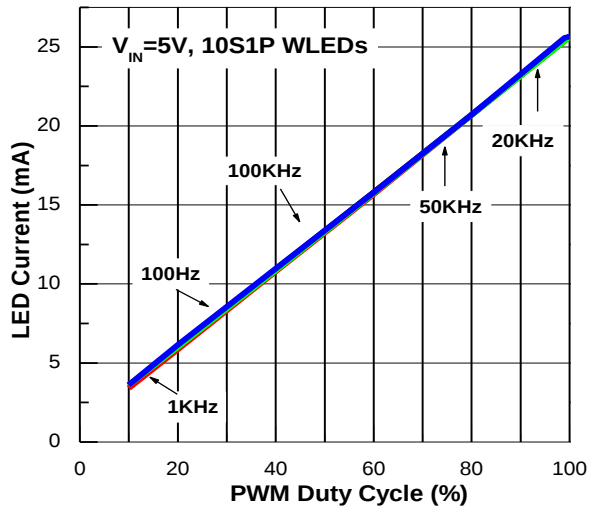


Figure10. LED Current vs. Duty Cycle

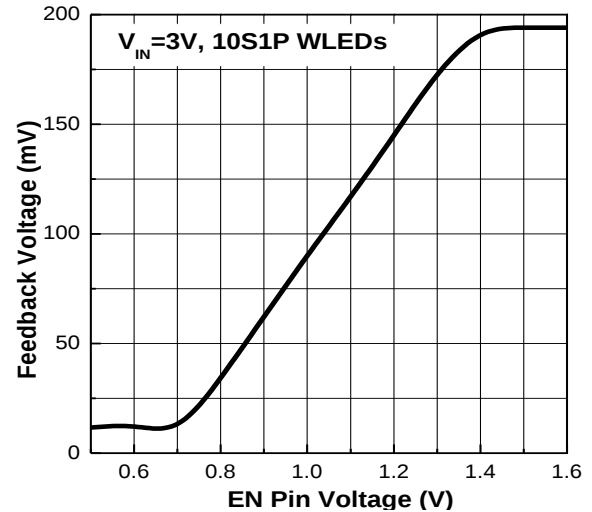


Figure11. Feedback Voltage vs. EN Voltage

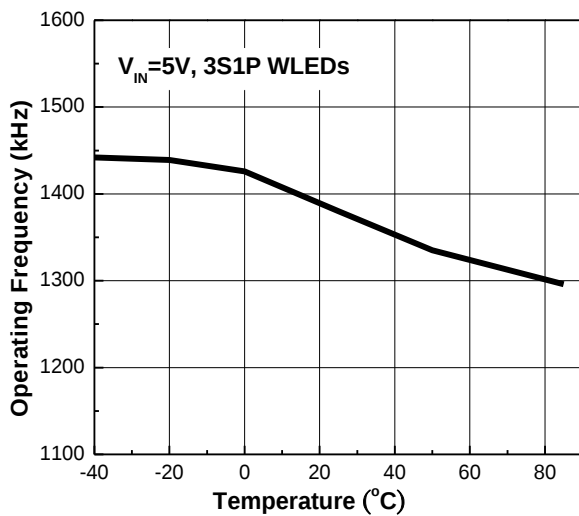


Figure12. Operating frequency vs. Temperature.

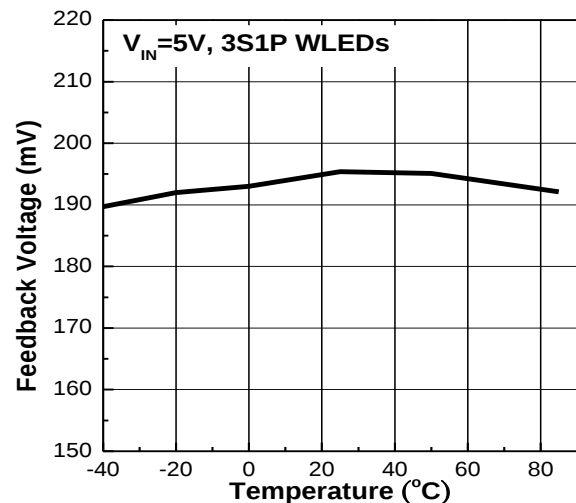


Figure13. Feedback Voltage vs. Temperature.

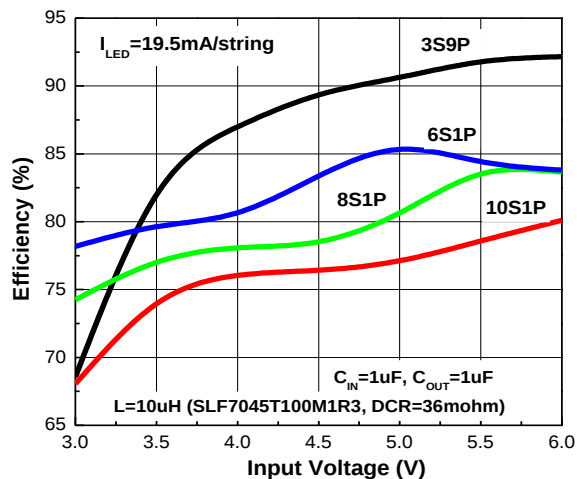


Figure14. Efficiency vs. Input Voltage.

Applications Information

Operation

The FP6745 is designed in a current mode, fixed-frequency pulse-width modulation (PWM) step-up converter to drive up to 10 series-connected WLEDs. The FP6745 operates well with a variety of external components. See the following sections to optimize external components for a particular application.

Inductor Selection

For most applications, a 4.7μH to 15μH inductor is recommended for general use. The inductor parameters, current rating, DCR and physical size, should be considered. The DCR of inductor affects the efficiency of the converter. The inductor with lowest DCR is chosen for highest efficiency. The saturation current rating of inductor must be greater than the switch peak current, typically 1.33A. These factors affect the efficiency, output load capability, output voltage ripple and cost.

The inductor selection depends on the switching frequency and current ripple by the following formula:

$$L \geq \frac{V_{IN}}{f_{SW} \times \Delta I_L} \left(1 - \frac{V_{IN}}{V_{OUT}} \right)$$

Where f_{SW} is the 1.3MHz switching frequency.

ΔI_L is the inductor ripple current.

Capacitor Selection

The ceramic capacitor is ideal for FP6745 application. X5R or X7R types are recommended because they hold their capacitance over wide voltage and temperature ranges better than other Y5V or Z5U types. The input capacitor can reduce peak current and noise at power source. The output capacitor is typically selected based on the output voltage ripple requirements. For most applications, a 10μF input capacitors with a 0.47μF output capacitor are sufficient for general use. A higher or lower capacitance may be used depending on the acceptable noise level.

LED Current Setting

The LED current is specified by resistor from the FB pin to ground. In order to have accurate LED current, precision resistors are preferred (1% is recommended). The LED current can be programmed by: $I_{LED} = 195mV / R_{FB}$.

Over Voltage Protection

The FP6745 has an internal open-lamp protection circuit. In the cases of output open circuit, when the LEDs are disconnected from the circuit or the LEDs fail open circuit, the over-voltage function will monitor the output voltage through SW pin to protect the converter. The LED strings open will cause N-MOS to switch with a maximum duty cycle and come out output over-voltage. This may cause the SW voltage exceed its maximum rating then damage built-in N-MOS. In the state, the OVP protection circuitry will be triggered if output voltage exceeds 42V (typ.). The FP6745 will then stop switching and latch off. The FP6745 will automatically recover normal operation until EN input is recycled or the V_{IN} is re-started.

Dimming Control

There are two different types of dimming control circuits. The LED current can be set by modulating the EN pin with a DC voltage or a PWM signal.

(1) DC Voltage

The dimming control uses a DC voltage ranging from 0.7V to 1.4V which results in regulating FB voltage from 0V to 195mV respectively.

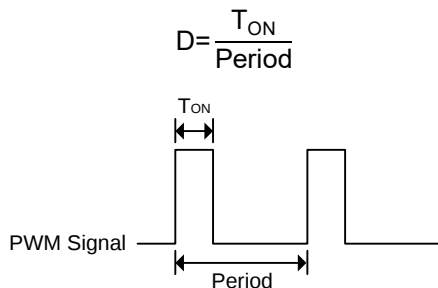
(2) PWM Signal

Changing the LED forward current not only changes the intensity of the LEDs but also the color. Controlling the intensity of the LEDs with a direct PWM signal allows dimming of the LEDs without changing the color.

Dimming the LEDs via a PWM signal essentially involves turning the LED on and off. The LEDs operate at either zero or full current. The amplitude of the PWM signal should be higher than the minimum EN dimming voltage (typically 1.4V). The LED average current increases proportionally with the duty cycle of the PWM signal. The color of the LEDs remains unchanged since the LED current value is either zero or a constant value. The dimming frequency of the PWM signal can be up to 50kHz and still retain well linearity. To avoid audio noise, dimming frequency greater than 20kHz is recommended. The PWM dimming on time (T_{ON}) must be greater than 1.5μs. For example, when dimming frequency is 40KHz, the cycle period

Applications Information (Continued)

will be 25μs. The PWM signal's duty cycle (D) must be greater than 6% to set T_{ON} greater than 1.5μs.



Layout Consideration

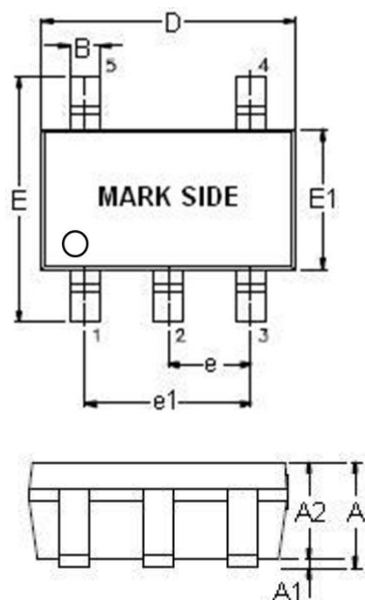
The proper PCB layout and component placement are critical for all switching regulators. The careful attention should be taken to the high-frequency and high current loops to prevent electromagnetic interference (EMI) problems. Here are some suggestions to the layout of FP6745 design.

- The input capacitor should be located as close to the VIN and GND pin as possible.
- Minimize the distance of all traces connected to the SW node. The external components, C_{OUT}, L1 and D1 should be placed as close to the device as possible with short and wide route to obtain optimum efficiency.
- Keep the noise-sensitive feedback circuitry away from the switching node. Place feedback resistor as close to FB pin as possible.
- The ground terminal of C_{OUT} must be located as closed to GND pin as possible. Place C_{OUT} next to Schottky diode.

In Backlight application, the system engineers usually place the C_{OUT} close to the LED connector. The far C_{OUT} of the FP6745 may result in variable VFB. It's recommended to add one more C_{OUT} close to FP6745.

Outline Information

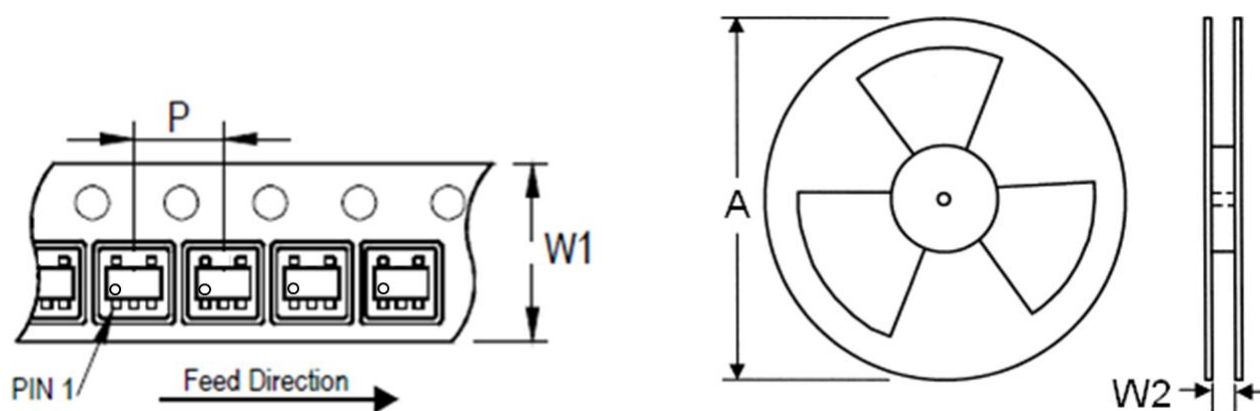
TSOT-23-5 Package (Unit: mm)



SYMBOLS UNIT	DIMENSION IN MILLIMETER	
	MIN	MAX
A	0.70	0.90
A1	0.00	0.10
A2	0.70	1.00
B	0.30	0.50
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.70
e	0.90	1.00
e1	1.80	2.00
L	0.30	0.60

Note 4: Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.3mm.
 Note 5: Reference JEDEC MO-193.

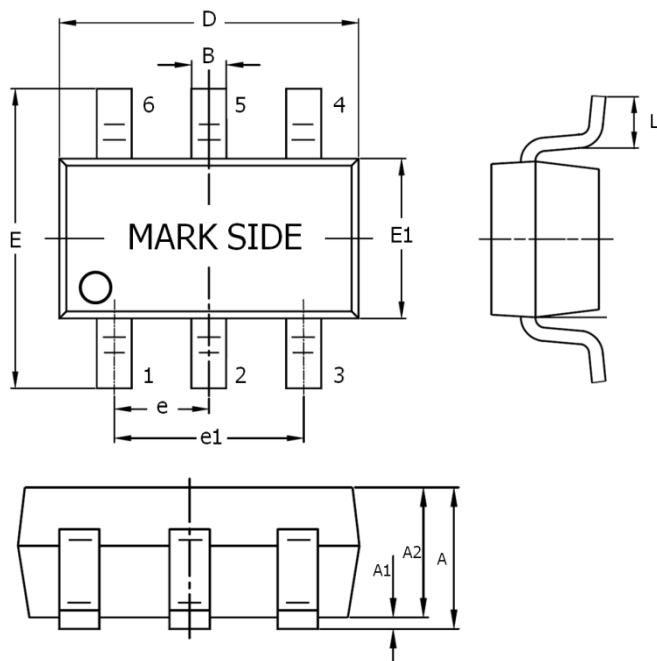
Carrier Dimensions



Tape Size (W1) mm	Pocket Pitch (P) mm	Reel Size (A)		Reel Width (W2) mm	Empty Cavity Length mm	Units per Reel
		in	mm			
8	4	7	180	8.4	300~1000	3,000

Outline Information (Continued)

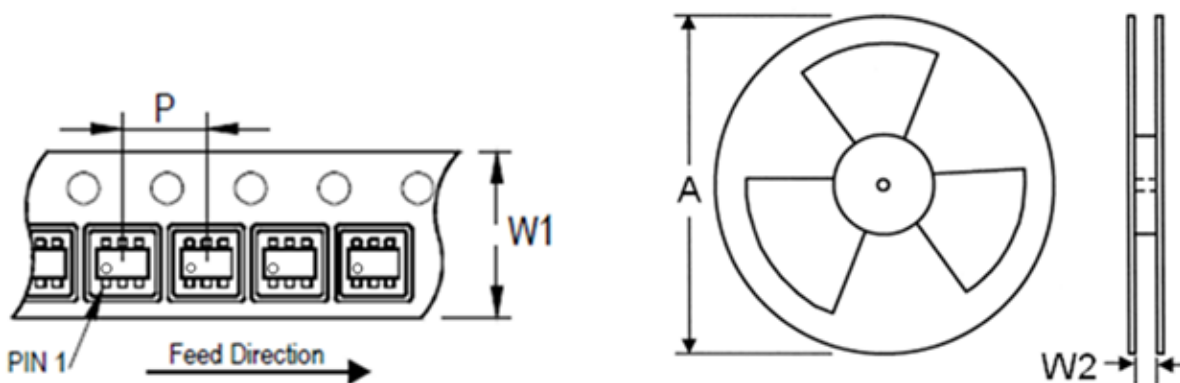
TSOT-23-6 Package (Unit: mm)



SYMBOLS UNIT	DIMENSION IN MILLIMETER	
	MIN	MAX
A	0.70	0.90
A1	0.00	0.10
A2	0.70	1.00
B	0.30	0.50
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.70
e	0.90	1.00
e1	1.80	2.00
L	0.30	0.60

Note 6: Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.3mm.
 Note 7: Reference JEDEC MO-193.

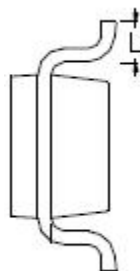
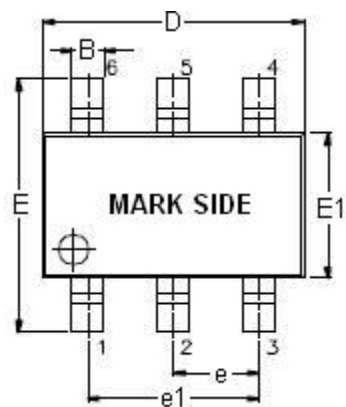
Carrier Dimensions



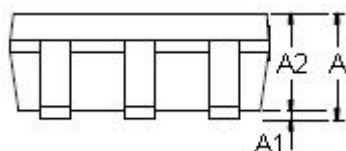
Tape Size (W1) mm	Pocket Pitch (P) mm	Reel Size (A)		Reel Width (W2) mm	Empty Cavity Length mm	Units per Reel
		in	mm			
8	4	7	180	8.4	300~1000	3,000

Outline Information (Continued)

SOT-23-6 Package (Unit: mm)



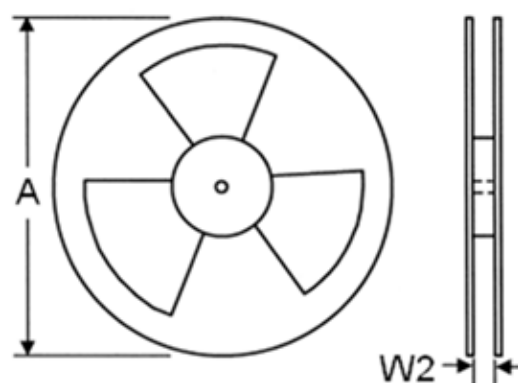
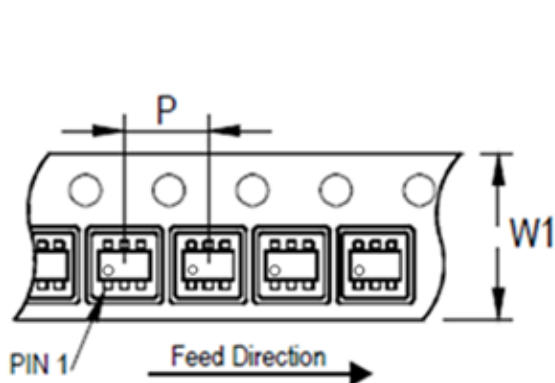
SYMBOLS UNIT	DIMENSION IN MILLIMETER	
	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
B	0.30	0.50
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.70
e	0.90	1.00
e1	1.80	2.00
L	0.30	0.60



Note 8: Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.3mm.

Note 9: Reference JEDEC MO-178.

Carrier Dimensions



Tape Size (W1) mm	Pocket Pitch (P) mm	Reel Size (A)		Reel Width (W2) mm	Empty Cavity Length mm	Units per Reel
		in	mm			
8	4	7	180	8.4	300~1000	3,000

Life Support Policy

Fitipower's products are not authorized for use as critical components in life support devices or other medical systems.